

ABSTRACT

A surge suppression circuit having a plurality of energy dissipating elements such as metal oxide varistors wherein all of the varistors have a nominal voltage within less than approximately $\pm 5\%$ of one another. In a preferred embodiment, the nominal voltage of the
5 varistors will be within less than $\pm 1\%$ of one another and the surge suppression circuit will further include a thermal cutoff device.